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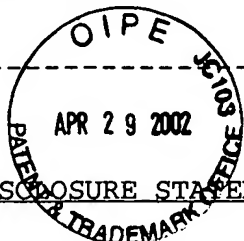
PATENT

Applicants: Robert Yin et al.
Assignee: Xilinx, Inc.
Title: METHOD AND SYSTEM FOR CONTROLLING DEHAU 2002 VALUES
OF FLIP-FLOPS IN PGA/ASIC-BASED DESIGNS
Serial No.: 10/082,630 File Date: 2-22-02
Examiner: Unassigned Art Unit: 2819
Docket No.: X-1070 US

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Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the twelve (12) references listed in the attached Form PTO-1449. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D. C. 20231, on April 18, 2002.

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APR 29 2002
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Complete if Known

Applicat n / Conf. N .	10/082,630 / 4447
Filing Date	February 22, 2002
First Named Inventor	Robert Yin
Art Unit	2819
Examiner Name	Unknown
Attorney Docket Number	X-1070 US

(use as many sheets as necessary)

Sheet	1	of	2
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Attorney Docket Number	X-1070 US
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Date Considered

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